

## Refine Search

### Search Results -

Terms	Documents
L32	0

Database:

US Pre-Grant Publication Full-Text Database  
 US Patents Full-Text Database  
 US OCR Full-Text Database  
 EPO Abstracts Database  
 JPO Abstracts Database  
 Derwent World Patents Index  
**IBM Technical Disclosure Bulletins**

Search:

L33

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### Search History

DATE: Friday, July 16, 2004   [Printable Copy](#)   [Create Case](#)

<u>Set</u> <u>Name</u> <u>Query</u> side by side	<u>Hit</u> <u>Count</u>	<u>Set</u> <u>Name</u> result set
DB=TDDBD; PLUR=YES; OP=OR		
<u>L33</u> L32	0	<u>L33</u>
DB=DWPI; PLUR=YES; OP=OR		
<u>L32</u> L31	0	<u>L32</u>
DB=JPAB; PLUR=YES; OP=OR		
<u>L31</u> L30	0	<u>L31</u>
DB=EPAB; PLUR=YES; OP=OR		
<u>L30</u> L29	0	<u>L30</u>
DB=PGPB; PLUR=YES; OP=OR		
<u>L29</u> L28	0	<u>L29</u>
DB=USOC; PLUR=YES; OP=OR		
<u>L28</u> L27	0	<u>L28</u>
DB=USPT; PLUR=YES; OP=OR		
<u>L27</u> "lattice constant" same dislocation\$ same first same second	1	<u>L27</u>

*DB=USPT;TDBD; PLUR=YES; OP=OR*

L26 "lattice constant" same dislocation\$ same first same second

1 L26

L25 "lattice constant" same dislocation\$ sane first and second

2250497 L25

*DB=TDBD; PLUR=YES; OP=OR*

L24 L23

0 L24

*DB=DWPI; PLUR=YES; OP=OR*

L23 L22

0 L23

*DB=JPAB; PLUR=YES; OP=OR*

L22 L21

0 L22

*DB=EPAB; PLUR=YES; OP=OR*

L21 L20

0 L21

*DB=USOC; PLUR=YES; OP=OR*

L20 L19

0 L20

*DB=PGPB; PLUR=YES; OP=OR*

L19 L18

0 L19

*DB=USPT; PLUR=YES; OP=OR*

L18 L17 and strain

1 L18

L17 L16 and (HTS same "a-b plane" same ("c-direction" OR "c direction"))

2 L17

L16 superconduct\$5

13402 L16

L15 superconduct\$5p

0 L15

L14 6630426

1 L14

*DB=TDBD; PLUR=YES; OP=OR*

L13 L12

0 L13

L12 L11

0 L12

*DB=DWPI; PLUR=YES; OP=OR*

L11 L10

0 L11

*DB=JPAB; PLUR=YES; OP=OR*

L10 L9

0 L10

*DB=EPAB; PLUR=YES; OP=OR*

L9 L8

0 L9

*DB=USOC; PLUR=YES; OP=OR*

L8 L7

0 L8

*DB=PGPB; PLUR=YES; OP=OR*

L7 L6

0 L7

*DB=USPT; PLUR=YES; OP=OR*

L6 L4 and ("n type polysilicon" and "p type polysilicon")

0 L6

L5 L4 and ("n-type polysilicon" and "p-type polysilicon")

0 L5

L4 L3 and polysilicon

8 L4

L3 (vertical near transistor\$) and (TFT OR SOI) and ("double gated" OR "common channel" OR "multi gated" OR "multi gate")

9 L3

L2 L1

2 L2

L1 6586296

2 L1

END OF SEARCH HISTORY

<u>L17</u>	L16 and (HTS same "a-b plane" same ("c-direction" OR "c direction"))	2	<u>L17</u>
<u>L16</u>	superconduct\$5	13402	<u>L16</u>
<u>L15</u>	superconduct\$5p	0	<u>L15</u>
<u>L14</u>	6630426	1	<u>L14</u>
<i>DB=TDBD; PLUR=YES; OP=OR</i>			
<u>L13</u>	L12	0	<u>L13</u>
<u>L12</u>	L11	0	<u>L12</u>
<i>DB=DWPI; PLUR=YES; OP=OR</i>			
<u>L11</u>	L10	0	<u>L11</u>
<i>DB=JPAB; PLUR=YES; OP=OR</i>			
<u>L10</u>	L9	0	<u>L10</u>
<i>DB=EPAB; PLUR=YES; OP=OR</i>			
<u>L9</u>	L8	0	<u>L9</u>
<i>DB=USOC; PLUR=YES; OP=OR</i>			
<u>L8</u>	L7	0	<u>L8</u>
<i>DB=PGPB; PLUR=YES; OP=OR</i>			
<u>L7</u>	L6	0	<u>L7</u>
<i>DB=USPT; PLUR=YES; OP=OR</i>			
<u>L6</u>	L4 and ("n type polysilicon" and "p type polysilicon")	0	<u>L6</u>
<u>L5</u>	L4 and ("n-type polysilicon" and "p-type polysilicon")	0	<u>L5</u>
<u>L4</u>	L3 and polysilicon	8	<u>L4</u>
<u>L3</u>	(vertical near transistor\$) and (TFT OR SOI) and ("double gated" OR "common channel" OR "multi gated" OR "multi gate")	9	<u>L3</u>
<u>L2</u>	L1	2	<u>L2</u>
<u>L1</u>	6586296	2	<u>L1</u>

END OF SEARCH HISTORY

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L23	0

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<u>Set</u> <u>Name</u>	<u>Query</u>	<u>Hit</u> <u>Count</u>	<u>Set</u> <u>Name</u> result set
	<i>DB=TDBD; PLUR=YES; OP=OR</i>		
<u>L24</u>	<u>L23</u>	0	<u>L24</u>
	<i>DB=DWPI; PLUR=YES; OP=OR</i>		
<u>L23</u>	<u>L22</u>	0	<u>L23</u>
	<i>DB=JPAB; PLUR=YES; OP=OR</i>		
<u>L22</u>	<u>L21</u>	0	<u>L22</u>
	<i>DB=EPAB; PLUR=YES; OP=OR</i>		
<u>L21</u>	<u>L20</u>	0	<u>L21</u>
	<i>DB=USOC; PLUR=YES; OP=OR</i>		
<u>L20</u>	<u>L19</u>	0	<u>L20</u>
	<i>DB=PGPB; PLUR=YES; OP=OR</i>		
<u>L19</u>	<u>L18</u>	0	<u>L19</u>
	<i>DB=USPT; PLUR=YES; OP=OR</i>		
<u>L18</u>	<u>L17 and strain</u>	1	<u>L18</u>